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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/034,379	01/03/2002	Yoshirou Tsurugida	OKI.291	8667	
75	90 06/25/2002				
VOLENTINE FRANCOS, PLLC			EXAMINER		
Suite 150 12200 Sunrise Vally Drive			NHU, DAVID		
Reston, VA 20	0191		ART UNIT	PAPER NUMBER	
			2818		
			DATE MAILED: 06/25/2002	DATE MAILED: 06/25/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicati n No.	Applicant(s)		
		10/034,379	TSURUGIDA, YOSHIROU		
•	Office Action Summary	Examiner	Art Unit		
		David Nhu	2818		
Period for	The MAILING DATE of this communication app Reply	ears on the cover sheet with the c	orrespondence address		
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status					
1)□ F	Responsive to communication(s) filed on				
2a) 🗌 📑	This action is FINAL . 2b)⊠ Thi	is action is non-final.			
3) Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims					
4)⊠ Claim(s) <u>1-7</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1-4,6 and 7</u> is/are rejected.					
7) Claim(s) <u>5</u> is/are objected to.					
8) Claim(s) are subject to restriction and/or election requirement. Application Papers					
9) The specification is objected to by the Examiner.					
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.					
If approved, corrected drawings are required in reply to this Office action.					
12) The oath or declaration is objected to by the Examiner.					
Priority under 35 U.S.C. §§ 119 and 120					
13)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a)⊠ All b)☐ Some * c)☐ None of:					
1.	. Certified copies of the priority documents	s have been received.	•		
2.	. Certified copies of the priority documents	s have been received in Applicati	on No		
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
		•			
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).					
a) ☐ The translation of the foreign language provisional application has been received. 15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.					
Attachment(s)		_			
2) Notice o	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) tion Disclosure Statement(s) (PTO-1449) Paper No(s) <u>2.</u>	5) Notice of Informal F	r (PTO-413) Paper No(s) Patent Application (PTO-152)		
J.S. Patent and Trade PTO-326 (Rev. (tion Summary	Part of Paper No. 4		

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DETAILED ACTIONS

Claims 1-7 are present for examination.

Specifications

1. Page 4, line 6, "Fig. 1" should be - Fig. 1A-1E --

Also Page 5, line 6, "Fig. 1" should be - Fig. 1A-1E --

Claim Objection

2. Claim 1, line 13, "said oxide inhibitor film" should be - said oxidation inhibitor film --

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 1-4, 6-7 are rejected under U.S.C 103(a) as being unpatentable over Background of Invention (BOI) in view of Halliyal et al (6,319, 775 B1), and Numasawa (5,306,672).

 Regarding claim 1, BOI, pages 1-3 disclose a method of forming selectively oxidizing a silicon wafer comprises the steps of: covering each of whole areas of both surfaces of a silicon wafer by an oxidation inhibitor film (silicon nitride film) with interposition of a pad oxide film; patterning said pad oxide film and an oxidation inhibitor film on said pad oxide film on one surface of said wafer to form desired patterns to partially expose the one surface of said wafer through said patterns; removing said pad oxide film and said oxidation inhibitor film on said pad oxide film formed on the other surface of said wafer to expose the whole area of the other surface of said wafer; oxidizing the regions exposed partially on the one surface of said wafer

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and the whole area of the other surface of said wafer simultaneously to grow a silicon dioxide film on both surfaces of said wafer.

It is noted that BOI fails to teach the step of removing said oxidation inhibitor film overlying said pad oxide film and said underneath pad oxide film remaining on the one surface of said wafer. However, Halliyal, figures 1-4, and related on col. 1-8, (col. 5, lines 40-67, col. 6, lines 1-16) teach removing said oxidation inhibitor film (silicon nitride foilm) overlying said pad oxide film and said underneath pad oxide film remaining on the one surface of said wafer.

Also, Numasawa, figures 1-3, (col. 6, lines 24-35) teach removing said oxidation inhibitor film (silicon nitride foilm) overlying said pad oxide film and said underneath pad oxide film remaining on the one surface of said wafer.

Regarding claim 2, (BOI, pages 1-2) teaches the oxidation inhibitor film is a silicon nitride film. Regarding claim 3, (Halliyal, col. 5, lines 1-8, col. 6, lines 6-16) teaches the pad oxide film covering both surfaces of said wafer are formed in a batch-type thermal oxidation furnace/apparatus.

Regarding claim 4, (Halliyal, col. 6, lines 6-16) teaches the silicon nitride films covering both surfaces of said wafer with interposition of said pad oxide film are formed by a batch-type LPCVD.

Regarding claim 6, (BOI, col. 5, pages 1-2) teaches the oxide film partially formed on the one surface of said wafer is used isolation regions.

Regarding claims 7, (BOI, pages 1-2) teaches the oxide film formed on the whole area of the other surface of said wafer is used as a sacrifice layer to remove contamination, which occurs in handling of said wafer, by an etching process.

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It would have been obvious to one having ordinary skill in the art at the time of the present invention to apply the teachings of Halliyal and Numasawa into the method of BOI as they are related to the same subject matter of carrying out a LOCOS process effectively, a number of silicon wafers are held standing up on either edges or lying horizontal mutually spaced apart on a holder called a wafer boat and are transferred into an oxidation process which can be carried out in either a batch-type thermal oxidation furnace, or alternatively, in a single wafer oxidation apparatus.

Allowable Subject Matter

5. Claim 5 is **objected** to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

Claim 5 includes allowable subject matter since the prior made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations.

Because BOI, Numasawa'672, and Halliyal'775 taken individually or in combination, do not teach exposed regions on the one surface of said wafer and the exposed area on the other surface of said wafer are subjected to said oxidation process by a batch-type thermal oxidation furnace.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure: Park'227, Komori'730 are cited as of interest.

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7. A shortened statutory period for response to this action is set to expired 3 (three) months from the date of this letter. Failure to respond within the period for response will cause the application to become abandoned (see 710.02 (b)).

8. Any inquiry concerning this communication on earlier communications from the examiner should be directed to David Nhu, (703) 306-5796. The examiner can normally be reached on Monday-Friday from 7:30 AM to 5:00 PM. The examiner's supervisor, David Nelms can be reached on (703) 308-4910.

The fax phone number for the organization where this application or proceeding is assigned is (703) 308-7724.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956

David Nhu

June 21, 2002

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